

Navin Mubarak

Summary	Experienced electrical engineer with over twelve years software and hardware experience in the electronics/semiconductor.
Experience	<p>6/2007–8/22/2008 Denali Software, Inc. Nashua, NH Account Manager</p> <p>Responsible for a broad range of silicon IP and verification IP products. Silicon IP portfolio included solutions for DDR1/2/3 memory controllers, DDR PHYs, PCI Express cores, and NAND Flash memory controllers. Verification IP portfolio included memory models for all mainstream memory types as well as protocol verification IP for PCI Express, AMBA (AXI, AHB, APB), USB, Ethernet, PLB, OCP, and System RDL.</p> <p>11/2004–6/2007 LogicVision, Inc. Franklin, MA Senior Manager Applications Engineering</p> <p>Managed and assisted applications organization which provided support for silicon test and diagnostics solutions (DFT) including memory BIST, logic BIST, ATPG, Boundary Scan (IEEE1149.1 & 1149.6), and PLL/SERDES BIST. Supported customer base in excess of 100+ companies and gained detailed knowledge of various ASIC design methodologies and tools from RTL design phase through silicon bring-up and debug.</p> <ul style="list-style-type: none">• Managed international support team.• Member of product steering team responsible for new products, features, and improvements.• Responsible for pre-sales activities and post-sales support for Eastern US and Canada.• Assisted customer base with implementation and verification of design for test.• Involved with documentation, product review, and testing.• Developed classroom and online training material.

- Produced and managed online video training web site.
- Managed online support system used for all pre and post-sales support activity.
- Developed and implemented custom solutions for customers.

1/2004–11/2004 Silicon Dimensions, Inc. Marlborough, MA

Field Applications Engineer

Technical support engineer for Chip2Nite design planning tools which provided floorplanning, cell placement, routing, analysis, and optimization capabilities.

- Provided pre-sales and post-sales support to clients.
- Authored and presented technical sales material.
- Developed product documentation and performed product testing.
- Helped marketing and design teams determine product direction and feature sets which would best meet the needs of target customer base.
- Developed scripts to automate Chip2Nite design planning tool suite.

1999–1/2004 LogicVision, Inc. Franklin, MA

Manager Applications Engineering

- Managed international support team.
- Responsible for pre-sales activities and post-sales support for Eastern US and Canada.
- Assisted customer base with implementation and verification of design for test.
- Worked with customers and engineering teams to improve product feature set, ease of-use, and quality.
- Performed Field Applications Engineer duties described below.

Field Applications Engineer

- Authored and presented technical sales material.

- Developed lecture material for LogicVision training course.
- Primary instructor for product training courses.
- Coordinated Test Architecture Review meetings with customers to assess current project requirements and architect optimal test solution for their devices.
- Performed Post-Sales Support Engineer duties described below.

Post-Sales Support Engineer

- Provided post-sales support to key LogicVision clients.
- Developed initial version of environment setup and automation utility used to simplify LogicVision tool flow for end users.
- Authored applications notes on standard and customized test implementation flows for LogicVision clients.
- Performed local system administrator functions for North East sales office.

1996–1999 Sun Microsystems, Inc. Burlington, MA

Senior Design For Test Engineer

- DFT lead on high-speed, multi-frequency ASIC for SunFire™ family of products.
- Designed and implemented complete test solution including memory BIST, logic BIST, ATPG, JTAG, and custom scan dump circuitry.
- Performed top level chip integration including IO ring generation and sub-block connectivity.
- Generated scripts to automate top level integration and test insertion process.

Education

1994–1996 Worcester Polytechnic Institute Worcester, MA

- B.S., Electrical Engineering with Specialization Computer Engineering

1991–1994 Springfield Technical College Springfield, MA

- Associate, Science in Engineering
- Graduated Magna Cum Laude

Semiconductor Related Skills

- LogicVision Embedded Test Products, SunRise, Synopsys Design Compiler, PrimeTime, Formality & VCS, Cadence Verilog-XL & NC-Verilog, ModelSim, Debussy, SignalScan, Undertow, Chip2Nite
- Verilog, VHDL

Other Technical Skills

- Basic User: ASP.NET, AJAX, Framemaker, MacOSX, PASCAL
- Intermediate User: Acrobat, Apache, C/C++, CGI Programming, ClearCase, Dreamweaver, HTML, IIS, MySQL, Networking, PHP, Ruby, Salesforce, Shell Scripting, Solaris, Windows (NT, Vista)

Achievements

- Advanced User: CRM, Linux, SW/HW Installation, Linux, Microsoft Office Products, MSDOS, PERL, TCL, UNIX, Windows (95,98,2000,ME,XP,XP2003)
- US Patent (6678875) : Self-Contained Embedded Test Design Environment and Setup Utility
- LogicVision Customer Satisfaction Award